

## WEST Search History





DATE: Wednesday, November 21, 2007

Hide?	Set Name	Query	Hit Count
<i>DB=PGPB,USPT,USOC; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L28	L27 and @pd > 20070928	0
<input type="checkbox"/>	L27	oscillator and L23	4
<input type="checkbox"/>	L26	(phase difference) and L25	2
<input type="checkbox"/>	L25	(light source) and ("AC" voltage) near3 synchronized	16
<input type="checkbox"/>	L24	(light source) same ("AC" voltage) near3 synchronized	4
<input type="checkbox"/>	L23	L20 and ("AC" voltage) same (light source))	18
<input type="checkbox"/>	L22	L20 and (voltage synchron\$4)	8
<input type="checkbox"/>	L21	L20 and (voltage synchron\$)	12
<input type="checkbox"/>	L20	(345/102)! [CCLS]	1258
<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L19	L18 and @pd > 20070105	0
<input type="checkbox"/>	L18	L17 and (operational amplifier)	1
<input type="checkbox"/>	L17	L16 and (phase difference) and (phase comparator).	26
<input type="checkbox"/>	L16	(345/102  345/213)! [CCLS]	1969
<input type="checkbox"/>	L15	L12 and L8	39
<input type="checkbox"/>	L14	L13 and L8	0
<input type="checkbox"/>	L13	L12 and "XNOR" and "XOR"	92
<input type="checkbox"/>	L12	phase comparator	13469
<input type="checkbox"/>	L11	L10 and "XNOR" and "XOR"	0
<input type="checkbox"/>	L10	L9 and (frequency divider)	34
<input type="checkbox"/>	L9	(phase difference) and L8	102
<input type="checkbox"/>	L8	(345/213)! [CCLS]	726
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L7	L6 and L1	21
<input type="checkbox"/>	L6	L5 same clock	19192
<input type="checkbox"/>	L5	phase difference	134132
<i>DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L4	L3 and synchronization	26
<input type="checkbox"/>	L3	L2 and oscillator	74
<input type="checkbox"/>	L2	L1 and phase	825
<input type="checkbox"/>	L1	(345/102  345/104  345/84  345/87)! [CCLS]	2762

END OF SEARCH HISTORY